

What Is Claimed Is:

1. A liquid crystal display device comprising:
 - a plurality of gate lines and data lines arranged horizontally and vertically, respectively, for defining a plurality of pixel areas;
 - a plurality of switching devices formed at intersections of the gate lines and the data lines; and
 - a pixel electrode formed in a pixel area connected to the switching device corresponding to the pixel area and partially overlapping the data lines adjacent to the corresponding pixel area, wherein a first parasitic capacitance generated by the pixel electrode overlapping a data line for the corresponding pixel area and a second parasitic capacitance generated by the pixel electrode overlapping a data line for an adjacent pixel area are substantially equal to each other.
2. The liquid crystal display device according to claim 1, wherein a part of the data line for the corresponding pixel area protrudes into the corresponding pixel area and is overlapped by the pixel electrode.
3. The liquid crystal display device according to claim 1, wherein a part of the data line of the adjacent pixel area protrudes into the corresponding pixel area and is overlapped by the pixel electrode.
4. The liquid crystal display device according to claim 1, wherein a portion of the pixel electrode overlapping a data line has a taper shape.

5. The liquid crystal display device according to claim 1, wherein a portion of the data line of the corresponding pixel area protrudes into the corresponding pixel area and a portion of the pixel electrode overlapping a data line for the adjacent pixel area has a taper shape.

6. The liquid crystal display device according to claim 1, wherein the switching devices are thin film transistors.

7. The liquid crystal display device according to claim 6, wherein each of the thin film transistors comprises:

a gate electrode formed of a protrusion from the gate line;
an insulating layer deposited over an entire substrate on which the gate electrode is formed;
a semiconductor layer formed on the insulating layer;
source and drain electrodes formed on the semiconductor layer; and
a passivation layer deposited over the source and drain electrodes and the semiconductor layer.

8. The liquid crystal display device according to claim 7, wherein the source electrode protrudes into the pixel area and is overlapped by the pixel electrode.

9. The liquid crystal display device according to claim 7, wherein a portion of the pixel electrode protrudes and overlaps a source electrode of a thin film transistor for the corresponding pixel area.

10. The liquid crystal display device according to claim 1, further comprising a storage capacitor electrode crossing the corresponding pixel area in parallel to the gate line, wherein at least one of the data lines adjacent to the corresponding pixel area has a portion protruding under the pixel electrode and over the storage capacitor electrode.

11. A liquid crystal display device comprising:
a plurality of gate lines and data lines arranged horizontally and vertically defining a plurality of pixel areas;
a plurality of switching devices formed at intersections of the gate lines and the data lines; and

a pixel electrode formed in a pixel area connected to the switching device corresponding to the pixel area, each pixel electrode having a portion with a taper shape overlapping a data line of an adjacent pixel area, wherein a portion of the data line for the corresponding pixel area protrudes into the corresponding pixel area such that a first parasitic capacitance generated by the pixel electrode overlapping a data line of the corresponding pixel area and a second parasitic capacitance generated by the pixel electrode overlapping the data line for the adjacent pixel area are substantially equal to each other.

12. The liquid crystal display device according to claim 11, wherein the switching devices are thin film transistors.

13. The liquid crystal display device according to claim 12, wherein each of the thin film transistors comprises:

a gate electrode formed of a protrusion from the gate line;
an insulating layer deposited over an entire substrate on which the gate electrode is formed;
a semiconductor layer formed on the insulating layer;
source and drain electrodes formed on the semiconductor layer; and
a passivation layer deposited over the source and drain electrodes and the semiconductor layer.

14. The liquid crystal display device according to claim 11, further comprising a storage capacitor electrode crossing the corresponding pixel area in parallel to a gate line, wherein at least one of the data lines adjacent to the corresponding pixel area have a portion protruding under the pixel electrode and over the storage capacitor electrode.

15. A liquid crystal display device comprising:
a plurality of gate lines and data lines arranged horizontally and vertically, respectively, for defining a plurality of pixel areas;
a plurality of switching devices formed at intersections of the gate lines and the data

lines; and

a pixel electrode formed in a pixel area connected to a switching device corresponding to the pixel area and partially overlapping the data lines adjacent to the corresponding pixel area, wherein a first area of a data line for the corresponding pixel area overlapped with the pixel electrode and a second area of a data line for an adjacent pixel area overlapped with the pixel electrode are substantially equal to each other.

16. The liquid crystal display device according to claim 15, wherein a part of the data line for the corresponding pixel area protrudes into the corresponding pixel area and is overlapped by the pixel electrode.

17. The liquid crystal display device according to claim 15, wherein a portion of the pixel electrode overlapping a data line has a taper shape.

18. The liquid crystal display device according to claim 15, wherein a source electrode of the switching device protrudes into the pixel area and is overlapped by the pixel electrode.

19. The liquid crystal display device according to claim 15, wherein a portion of the pixel electrode protrudes and overlaps a source electrode of a switching device for the corresponding pixel area.

20. The liquid crystal display device according to claim 15, further comprising a storage capacitor electrode crossing the corresponding pixel area in parallel to a gate line, wherein at least one of the data lines adjacent to the corresponding pixel area have a portion protruding under the pixel electrode and over the storage capacitor electrode.